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		Application No.	10/752,131
		Filing Date	January 6, 2004
		First Named Inventor	Hae-Seung Lee
		Art Unit	2612
		Examiner Name	
Total Number of Pages in This Submission	7	Attorney Docket Number	16820P357

ENCLOSURES (check all that apply)

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Remarks		

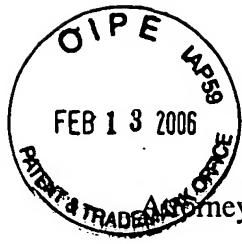
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name	Jeffrey T. Holman, Reg. No. 51,812 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	2/10/06

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Signature		Date	2/10/06



FEB 13 2006

Attorney's Docket No.: 16820P357

Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Hae-Seung Lee et al.

Application No.: 10/752,131

Filed: January 6, 2004

TC/A.U. 2612

Examiner: Unknown

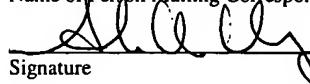
For: CMOS ACTIVE PIXEL WITH HARD
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CROSS REFERENCE UNDER 37 C.F.R. § 1.78 (a)(2)
TO RELATED APPLICATIONS

Sir:

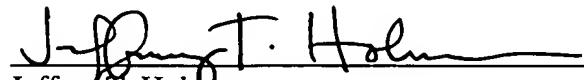
Pursuant to 37 C.F.R. § 1.78(a)(2), applicants hereby cross-reference the following related patent applications, which are assigned to the same assignee as the present patent application: U.S. Patent Application No. 10/752,112, filed on January 6, 2004 and U.S. Patent Application No. 10/752,197, filed on January 6, 2004.

If there are any additional charges, please charge Deposit Account No. 02-2666.

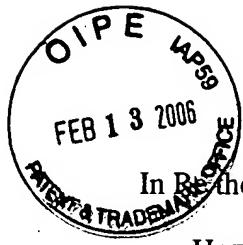
Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 2/10/06


Jeffrey T. Holman
Registration No. 51,812

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

HAE-SEUNG LEE, ET AL.

Application No.: 10/752,131

Filed: January 6, 2004

For: **CMOS ACTIVE PIXEL WITH HARD
AND SOFT RESET**

Art Group: 2612

Examiner:

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. §1.97

Commissioner for Patents
P.O. Box 1450
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In accordance with the duty of disclosure, enclosed is a copy of IDS Citation Form PTO/SB/08 or PTO-1449, together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed). This IDS and IDS Citation Form are being submitted before the mailing of a first Office Action. It is respectfully requested that the cited references be considered and that the enclosed copy of PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

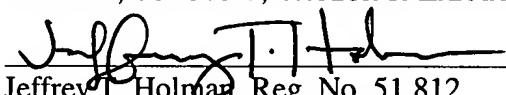
The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject application and is not to be construed as an admission that the information cited in this statement is material to patentability.

Please charge any fees due to Deposit Account 02-2666. A duplicate copy of the Fee Transmittal (PTO/SB/17) is enclosed for this purpose.

Respectfully submitted,

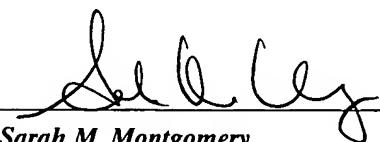
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

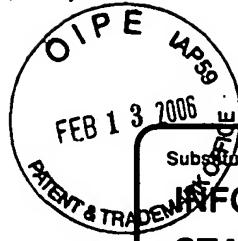
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Sarah M. Montgomery 2/10/06
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Sheet	1	of	2	Application Number	10/752,131
				Filing Date	January 6, 2004
				First Named Inventor	Hae-Seung Lee
				Art Unit	2612
				Examiner Name	
				Attorney Docket Number	16820P357

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Sheet

1

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Complete If Known

Application Number	10/752,131
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First Named Inventor	Hae-Seung Lee
Art Unit	2612
Examiner Name	
Attorney Docket Number	16820P357

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet

2

of

2

Complete If Known

Application Number	10/752,131
Filing Date	January 6, 2004
First Named Inventor	Hae-Seung Lee
Art Unit	2612
Examiner Name	
Attorney Docket Number	16820P357

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.*	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ^r
		DECKER et al., "A 256 x 256 CMOS Imaging Array with Wide Dynamic Range Pixels and Column-Parallel Digital Output", IEEE Journal of Solid-State Circuits, December 1998, Vol. 33, No. 12, pgs. 2081-2091	
		KOZLOWSKI et al., "A Progressive 1920x1080 Imaging System-on-Chip for HDTV Cameras", IEEE International Solid-State Circuits Conference, February 8, 2005, Session 19, Digest of Technical Papers, pgs. 358-359.	
		"Ultra-Pocket @ 5 Kit. Rapid Development Kit for 3.0-Megapixel Credit Card Cameras with Color TFT LCD Display", SMal Camera, 8/25/2005, pages 1-3, Technologies, http: www.smalcamera.com/up5lcd.html	
		"International Search Report", International Searching Authority, PCT/US2004/000153, July 29, 2004, 3 pages.	
		"Written Opinion of the International Searching Authority", International Searching Authority, PCT/US2004/000153, July 29, 2004, 8 pages.	

Examiner Signature	Date Considered
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